# INTERFERENCE CANCELLING RECEIVERS WITH GLOBAL MMSE-ZF STRUCTURE AND LOCAL MMSE OPERATIONS

Ahmet Baştuğ, Dirk T.M. Slock

Eurecom Institute

2229 Route des Crêtes, B.P. 193, 06904, Sophia Antipolis, Cedex, FRANCE ahmet.bastug@@eurecom.fr, dirk.slock@@eurecom.fr

#### ABSTRACT

In this paper we consider iterative techniques for multi-user detection. In the overdetermined case in which signal and noise subspaces exist, polynomial expansion can perhaps most conveniently be applied to a global MMSE ZF linear receiver. In the resulting iteration sections, we then propose to replace the various receiver components by a (possibly non-linear) MMSE variant, by interpreting the signal estimation functionality of each component. The resulting receiver sections, which can also be applied in the underdetermined case, have interpretations in terms of interference cancellation. We shall apply the proposed receiver strategy to the downlink in the FDD mode of UMTS, in which we consider a mobile terminal that also combats explicitly intercell interference.

# 1. BASEBAND DOWNLINK TRANSMISSION MODEL

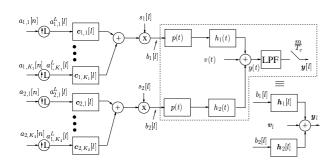


Figure 1: Baseband downlink transmission model

The baseband downlink transmission model of a CDMAbased cellular system with an interfering neighboring cell,  $BS_2$ , is shown in Fig. 1. In practice, however, this model on which we base our discussion can be generalized to multiple interfering base stations. For each base station  $BS_j$ , s.t.  $j \in \{1, 2\}$ , the  $K_j$  linearly modulated user signals are transmitted over the same linear multipath channel  $h_j(t)$ 

since we assume that downlink user chip sequences are synchronous and there is no beamforming. The symbol and chip periods T and  $T_c$  are related through the spreading factor L as  $T=L \times T_c$ , which here is assumed to be common for all the users and for the two base stations. In fact, we interpret this L to correspond to the highest spreading factor in the system, modeling each users' code of spreading factor,  $\mathcal{L}_{i,k}$ , as being equivalent to a combination of  $L/\mathcal{L}_{i,k}$ orthogonal codes with length L (or, for some simple receiver structures, equivalent to a code of spreading factor L with  $L/\mathcal{L}_{j,k}$  times higher power). The total chip sequences  $b_j[l]$  are the sums of the chip sequences of all the users for  $BS_j$ . Every user chip sequence  $b_{j,k}[l]$  is given by the convolution of the L times upsampled form,  $a_{j,k}^{L}[l]$ , of the symbol sequence  $a_{j,k}[n]$  (s.t.  $n = \lfloor \frac{l}{L} \rfloor$ ) and an aperiodic spreading sequence  $w_{j,k}[l]$  which is itself (UMTS FDD mode) the product of a periodic, unit energy Walsh-Hadamard spreading sequence  $c_{i,k}[l \mod L]$ , and a base-station specific unit magnitude aperiodic complex scrambling sequence  $s_j[l]$  as  $w_{j,k}[l] = c_{j,k}[l \mod L]s_j[l]:$ 

$$b_{j}[l] = \sum_{k=1}^{K_{j}} b_{j,k}[l] = \sum_{k=1}^{K_{j}} \sum_{i=-\infty}^{\infty} a_{j,k}^{L}[i]w_{j,k}[l-i], \ j \in \{1,2\}.$$
(1)

The chip sequences  $b_1[l]$  and  $b_2[l]$  pass through similar pulse shape filters p(t), their corresponding propagation channels  $h_1(t)$  and  $h_2(t)$  and the antialiasing filter at the receiver front end before getting sampled. After sampling, these two overall continuous time transmission channels can be interpreted as discrete multi-channels by the mobile receiver if the signal is captured by q sensors and/or sampled at an integer multiple m of the chip rate, rendering the total number of samples per chip  $mq \ge 1$ . Stacking these mq samples in vectors, we get the sampled received vector signal

$$\begin{aligned} \mathbf{y}[l] &= \mathbf{y}_1[l] + \mathbf{y}_2[l] + \mathbf{v}[l] \\ \mathbf{y}_j[l] &= \sum_{k=1}^{K_j} \sum_{i=0}^{N-1} \mathbf{h}_j[i] b_{j,k}[l-i] \quad j \in \{1,2\} \quad (2) \end{aligned}$$

where

$$\boldsymbol{y}_{j}[l] = \begin{bmatrix} y_{j,1}[l] \cdots y_{j,mq}[l] \end{bmatrix}^{T},$$

$$\boldsymbol{h}_{j}[l] = \begin{bmatrix} h_{j,1}[l] \cdots h_{j,mq}[l] \end{bmatrix}^{T},$$

$$\boldsymbol{v}[l] = \begin{bmatrix} v_{1}[l] \cdots v_{mq}[l] \end{bmatrix}^{T}.$$

$$(3)$$

Here, with a slight abuse of notation,  $h_j[l]$  represents the vectorized samples (represented at chip rate) of the overall channels (assumed to have the same delay spread of N chips) including the pulse shape, the propagation channel and the antialiasing receiver filter.

When we model the scramblers as unknown, i.i.d, aperiodic sequences and the symbol sequences as i.i.d., stationary, white sequences, then the chip sequences  $b_{\{1,2\}}[l]$  are also stationary and white. Therefore, both the intracell and intercell contribution to y[l] are stationary vector processes the continuous-time counterparts of which are cyclostationary with chip period. Finally, as the remaining noise, v[l], is also assumed to be white and stationary, the sum of interference and noise is a stationary chip rate vector process.

### 2. POLYNOMIAL EXPANSION (PE) AND ITERATIVE INTERFERENCE CANCELLING (IC) STRUCTURES

In this section, we develop intra and intercell interference cancelling (IC) structures based on polynomial expansion (PE), see [1] for an introduction to PE.

Assuming without loss of generality that there is a single highly interfering BS, i.e.  $BS_2$ , a vector of received signal over one symbol period can be written as

$$\mathbf{Y}[n] = [\mathbf{H}_{1}(z)\mathbf{S}_{1}[n]\mathbf{C}_{1} \quad \mathbf{H}_{2}(z)\mathbf{S}_{2}[n]\mathbf{C}_{2}] \begin{bmatrix} \mathbf{A}_{1}[n] \\ \mathbf{A}_{2}[n] \end{bmatrix} + \mathbf{V}[n]$$
$$= \widetilde{\mathbf{G}}(n,z) \quad \mathbf{A}[n] + \mathbf{V}[n] \qquad (4)$$

representing the system at the symbol rate.

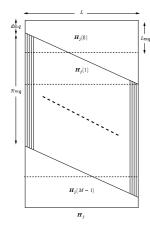


Figure 2: Channel impulse response of  $H_j(z)$ .

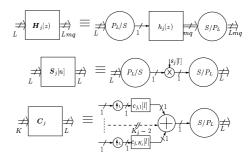


Figure 3: Symbol rate and chip rate equivalent representations

As shown in Fig. 2,  $\boldsymbol{H}_{j}(z) = \sum_{i=0}^{M_{j}-1} \boldsymbol{H}_{j}[i] z^{-i}$  is the symbol rate  $Lmq \times L$  channel transfer function,  $z^{-1}$ being the symbol period delay operator. The block coefficients  $H_j[i]$  are the  $M_j = \lceil \frac{L+N_j+d_j-1}{L} \rceil$  parts of the block Toeplitz matrix with  $mq \times 1$  sized blocks,  $h_i$  being the first column whose top entries might be zero for it comprises the transmission delay  $d_j$  between  $BS_j$  and the mobile terminal. The  $L \times L$  matrices  $S_i[n]$  are diagonal and contain the scrambler of  $BS_i$  for symbol period n. The column vectors  $A_j[n]$  contain the  $K_j$  symbols of  $BS_j$ , A[n] contains the total  $K = K_1 + K_2$  user symbols in two base stations, and  $C_j$  is the  $L \times K_j$  matrix of the  $K_j$  active codes for  $BS_i$ . Based on the equivalency of the chip rate and the symbol rate representations as demonstrated in Fig. 3 which shows the conversion between the two representations via serial-to-parallel and parallel-to-serial converters by vectorizing and sample rate conversion by a factor L, one can pass from the chip level models represented by (1), (2) and (3) to the symbol level model represented by (4) where G(n, z)is a  $Lmq \times K$  channel-plus-spreading symbol rate filter. Although it is possible to find an FIR left inverse filter for G(n, z) provided that  $Lmq \ge K$ , this is not practical since  $\widetilde{m{G}}(n,z)$  is time-varying due to the aperiodicity of the scrambling. Therefore, we will introduce a less complex approximation to this inversion based on the polynomial expansion technique [1]. Instead of basing the receiver directly on the received signal, we shall first introduce a dimensionality reduction step (from Lmq to K) by equalizing the channels with minimum mean square error zero forcing (MMSE-ZF) chip rate equalizers  $F_{j}(z)$  followed by a bank of correlators [3]. Let X[n] be the  $K \times 1$  correlator output, which would correspond to the Rake receiver outputs if channel matched filters were used instead of channel equalizers. Then,

$$\begin{aligned} \boldsymbol{X}[n] &= \quad \boldsymbol{\hat{F}}(n,z)\boldsymbol{Y}[n] \\ &= \begin{bmatrix} \boldsymbol{C}_1^H \boldsymbol{S}_1^H[n] \boldsymbol{F}_1(z) \\ \boldsymbol{C}_2^H \boldsymbol{S}_2^H[n] \boldsymbol{F}_2(z) \end{bmatrix} (\boldsymbol{\tilde{G}}(n,z)\boldsymbol{A}[n] + \boldsymbol{V}[n]) \\ &= \quad \boldsymbol{M}(n,z)\boldsymbol{A}[n] + \boldsymbol{\tilde{F}}(n,z)\boldsymbol{V}[n] \end{aligned}$$

where  $M(n,z) = \widetilde{F}(n,z)\widetilde{G}(n,z)$  and ZF equalization re-

sults in  $F_j(z)H_j(z) = I$ . Hence,

$$\boldsymbol{M}(n,z) = \sum_{i=-\infty}^{\infty} \boldsymbol{M}[n,i] z^{-i} = \begin{bmatrix} I & * \\ * & I \end{bmatrix}$$

due to proper normalization of the code energies.

In order to obtain the estimate of A[n], we initially consider the processing of X[n] by a decorrelator as

$$\widehat{\boldsymbol{A}}[n] = \boldsymbol{M}(n, z)^{-1} \boldsymbol{X}[n] = (\boldsymbol{I} + \overline{\boldsymbol{M}}(n, z))^{-1} \boldsymbol{X}[n].$$

The correlation matrix M(n, z) has a coefficient M[n, 0] with a dominant unit diagonal in the sense that all other elements of the M[n, i] are much smaller than one in magnitude. Hence, the polynomial expansion approach suggests to develop  $(I + \overline{M}(n, z))^{-1} = \sum_{i=0}^{\infty} (-\overline{M}(n, z))^i$  up to some finite order, which leads to

$$\widehat{\boldsymbol{A}}^{(-1)}[n] = 0$$

$$i \ge 0 \quad \widehat{\boldsymbol{A}}^{(i)}[n] = \boldsymbol{X}[n] - \overline{\boldsymbol{M}}(n, z) \, \widehat{\boldsymbol{A}}^{(i-1)}[n]$$

In practice, we stop at the first-order expansion, the quality of which depends on the degree of dominance of the diagonal of the static part of M(n, z) with respect to its off-diagonal elements and the dynamic part.

At first order, the expression for the user of interest (user one) becomes:

$$\begin{aligned} \widehat{\boldsymbol{a}}_{1,1}[n] &= \boldsymbol{e}_{1}^{H} \widehat{\boldsymbol{A}}^{(1)}[n] \\ &= \boldsymbol{e}_{1}^{H} (\boldsymbol{X}[n] - \overline{\boldsymbol{M}}(n, z) \widehat{\boldsymbol{A}}^{(0)}[n]) \\ &= \boldsymbol{e}_{1}^{H} (2\boldsymbol{X}[n] - \boldsymbol{M}(n, z) \boldsymbol{X}[n]) \\ &= \boldsymbol{e}_{1}^{H} \widetilde{\boldsymbol{F}}(n, z) (2\boldsymbol{Y}[n] - \widetilde{\boldsymbol{G}}(n, z) \boldsymbol{X}[n]) \\ &= \boldsymbol{e}_{1,1}^{H} \boldsymbol{S}_{1}^{H}[n] \boldsymbol{F}_{1}(z) (2\boldsymbol{Y}[n] - \widetilde{\boldsymbol{G}}(n, z) \widetilde{\boldsymbol{F}}(n, z) \boldsymbol{Y}[n]) \end{aligned}$$

where  $e_i$  ( $i \in \{1, 2, ..., K\}$ ) is a column vector having a 1 at the  $i^{th}$  position and the rest filled with 0s. From this symbol rate equation, one can obtain the chip rate signal processing diagram in Fig. 4 by using the equivalencies in Fig. 3. Each branch in the IC block is formed in order by a linear filter, a downsampler, a descrambler, a serial to parallel converter, a despreader, a respreader, a parallel to serial converter, a scrambler, an upsampler and a re-channeling filter.

Now that we have obtained the first order *polynomial expansion decorrelating structure*, we look for an *equivalent interference cancelling representation* that excludes the user signal estimation operation in the first IC branch [4]. Let

$$I_{K \times K} = [e_1 e_2 \dots e_K] = [e_1 \overline{e}_1]$$
$$\widetilde{G}(n, z) = [\widetilde{g}_1(n, z) \overline{G}_1(n, z)]$$
$$\widetilde{F}(n, z) = [\widetilde{f}_1(n, z)^T \overline{F}_1(n, z)^T]^T$$
$$A[n] = [a_{1,1}[n] \overline{A}_1[n]^T]^T$$
$$\widetilde{f}_1(n, z) \widetilde{g}_1(n, z) = 1.$$

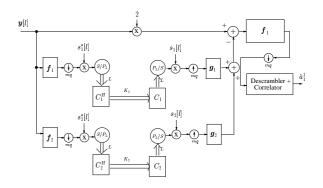


Figure 4: Chip rate first order polynomial expansion structure.

Then, dropping the indices and starting from (5), we obtain

$$\begin{aligned} \widehat{a}_{1,1} &= (2\widetilde{f}_1\widetilde{g}_1 - \widetilde{f}_1\widetilde{g}_1\widetilde{f}_1\widetilde{g}_1 - \widetilde{f}_1\overline{G}_1\overline{F}_1\widetilde{g}_1)a_{1,1} + \\ &(2\widetilde{f}_1\overline{G}_1 - \widetilde{f}_1\widetilde{g}_1\widetilde{f}_1\overline{G}_1 - \widetilde{f}_1\overline{G}_1\overline{F}_1\overline{G}_1)\overline{A}_1 \\ &= (\widetilde{f}_1\widetilde{g}_1 - \widetilde{f}_1\overline{G}_1\overline{F}_1\widetilde{g}_1)a_{1,1} \\ &+ (\widetilde{f}_1\overline{G}_1 - \widetilde{f}_1\overline{G}_1\overline{F}_1\overline{G}_1)\overline{A}_1 \\ &= \widetilde{f}_1(I - \overline{G}_1\overline{F}_1)Y \\ &= e_1^H\widetilde{F}(Y - \widetilde{G}\overline{e}_1\overline{e}_1^H\widetilde{F}Y) \\ &= e_1^H\widetilde{F}(Y - \overline{G}_1\overline{F}_1Y). \end{aligned}$$

This first order estimation process has now the form of an interference canceller as shown in Fig. 5. Different from the polynomial expansion structure, there is here no multiplicative factor of two at the top line and  $\overline{C}_1$ ,  $\overline{C}_1^H$  correspond to (de)spreading with the intracell interferer's codes, excluding the code of interest. So, the top IC branch handles intracell interference whereas the bottom IC branch handles intercell interference. We also made some changes like introducing nonlinear processors  $\zeta(.)$  in between the despreader and the spreader modules and replacing  $f_1$  after the subtractor by  $f_3$ , the purposes of which will be explained later.

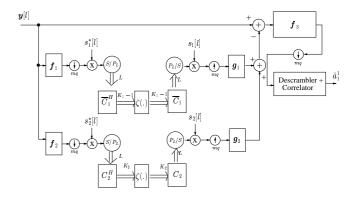


Figure 5: Chip rate first order interference canceller structure

#### 3. LOCAL MMSE OPERATIONS

One of the advantages of the MMSE-ZF approach w.r.t. the MMSE approach is that clear symbol and chip sequence estimates appear at various points in the receiver which can be improved locally by replacing whatever the global MMSE-ZF structure yields as estimates by improved estimates in the MMSE sense. Any local MMSE improvement should lead to global MMSE improvement. In an iterative PE approach, such modifications should also lead to smaller offdiagonal power and hence faster convergence of the iterations to an estimate that is closer to a MMSE estimate. The interpretations to be discussed though being applicative to any iteration except for a possible dependence of a number of quantities on the iteration index, we will concentrate only on the first iteration displayed in Fig. 5 where the role of filters  $f_1$  and  $f_2$  is to produce estimates of the chip sequences of  $BS_1$  and  $BS_2$ . Those estimates can be improved by replacing the MMSE-ZF chip rate equalizers  $\boldsymbol{f}_1$  and  $\boldsymbol{f}_2$  considered so far by MMSE equalizers which, though they perturb the orthogonality of the codes, do not enhance as much the intercell interference plus noise [2]. The estimated chip sequence then gets descrambled and passes by correlators to produce symbol estimates for the intracell/intercell interferers. These symbol estimates can be improved in a variety of ways by static linear or nonlinear functions  $\zeta(.)$  such as to exploit the symbol variance to introduce a LMMSE weighting factor or to exploit the symbol constellation by taking hard decisions which however may not improve the estimate. Hence, one may replace the hard decision by a variety of soft decisions. A locally optimal MMSE estimate is obtained by using a hyperbolic tangent function and the estimated symbols are then respread, scrambled and added to produce again an estimate of the chip sequence. The purpose of the rechanneling filters  $g_i$  (in the PE so far equivalent to the channel impulse response  $h_i$  is to produce an intracell/intercell interference estimate at the level of the received signal, on the basis of the chip sequences estimates. The linear filter after the IC canceller,  $f_3$ , which is considered to be equal to  $f_1$  in the polynomial expansion setup, can also be (in fact must be in case of local MMSE modifications on the IC branches) replaced by a Rake or a LMMSE filter (taking into account reduced interference level). The second one would perform better but it requires the estimation of structured residual interferences from the two base stations.

For the spreading and despreading with the Walsh-Hadamard codes of the active users, it is suggested to (de)spread with all WH codes simultaneously, which can be done with the Fast Walsh-Hadamard transform (FWHT). Presence or absence of codes can be detected by estimating the powers at the correlator outputs.

So far we have considered a unique spreading factor.

The multirate case corresponding to variable spreading factors can be reformulated as a multicode case. The tree structure of the orthogonal variable spreading factors (OVSFs) allows to interpret the interfering users as equivalent low rate users (at least if the symbol constellation of the interferers will not get exploited, in which case only secondorder statistics count). Hence in that case one needs at most to consider the multicode case at low rate for the user of interest. If on the other hand one also desires to exploit the interferers' symbol constellation, then one needs to detect and use the actual spreading factors of the interferers. The tree structure of the OVSFs can be exploited to progressively explore increasing spreading factors and investigate the finite alphabet hypothesis at the various tree branches. In this case one cannot use the FWHT as such but the ingredients leading to its derivation (OVSF in fact) can still be used for fast (de)spreading.

The implementation of the hyperbolic tangent and the hard decisions requires the estimation of the user symbol powers. Let  $z_{i,j}$  represent the chip-rate channel-MMSE filter cascade of  $h_j$  and  $f_i$  including the external (up/down) samplers. Then, the expected value of the received power after the  $k^{th}$  decorrelator on the first branch is equal to

$$\begin{split} \beta_{1,k} &= |z_{1,1}(0)|^2 |a_{1,k}|^2 + \frac{1}{L} \left( ||z_{1,1}||^2 - |z_{1,1}(0)|^2 \right) \\ &\sum_{i=1}^{K_1} |a_{1,i}|^2 + \frac{1}{L} ||z_{1,2}||^2 \sum_{i=1}^{K_2} |a_{2,i}|^2 + \sigma_v^2 ||f_1||^2 \\ &= |z_{1,1}(0)|^2 |a_{1,k}|^2 + \left( ||z_{1,1}||^2 - |z_{1,1}(0)|^2 \right) \sigma_1^2 \\ &+ ||z_{1,2}||^2 \sigma_2^2 + \sigma_v^2 ||f_1||^2 \\ &= |z_{1,1}(0)|^2 |a_{1,k}|^2 + \sigma_{n1}^2 \end{split}$$

since the expected value of the correlation coefficient between any two nonorthogonal codes or between two shifted versions of the same code is equal to 1/L; zero-forced channel coefficient is  $z_{1,1}(0)$  (which is in fact 1 for unbiased MMSE case). We assume that we know the channel parameters, the total received power from both base stations and the noise variance but we do not know each single user's power. Therefore, the estimate of  $|a_{1,k}|^2$ , i.e.  $|\alpha_{1,k}|^2$ , can be calculated by subtracting the effective interference  $\sigma_{n1}^2$  from the long term average power  $\beta_{1,k}$  and scaling by  $1/|z_{1,1}(0)|^2$ . If the result is negative we replace it by 0. The powers of users in the second cell are estimated similarly.

For the MMSE filter construction after the subtractor, estimation of total residual interference powers originating from the two base stations are necessary. Let  $\gamma_{1int}$ ,  $\gamma_{2int}$  and  $\sigma_{user1}^2$  be the average chip rate measured powers before the rescrambler blocks in the two IC branches and the user chip rate power respectively. Then the residual powers are  $\sigma_{leak1}^2 = |\sigma_1^2 - \sigma_{user1}^2 - \gamma_{1int}|$  and  $\sigma_{leak2}^2 = |\sigma_2^2 - \gamma_{2int}|$ .

In the sequel, for simulations we will assume that the symbols are from a QPSK constellation. Hence we give here the optimal hyperbolic tangent estimator and the hard estimator for received QPSK symbols. Let  $r_{j,k} = \chi_{j,k} + i \psi_{j,k}$  ( $i = \sqrt{-1}$ ) be the correlator output for code k from  $BS_j$ , then

$$\widehat{\boldsymbol{a}}_{j,k}^{thyp} = \frac{\alpha_{j,k}}{\sqrt{2}} \tanh(\sqrt{2} \mid z_{1,1}(0) \mid \frac{\alpha_{j,k}}{\sigma_{nj}^2} \chi_{j,k})$$

$$+ i \frac{\alpha_{j,k}}{\sqrt{2}} \tanh(\sqrt{2} \mid z_{1,1}(0) \mid \frac{\alpha_{j,k}}{\sigma_{nj}^2} \psi_{j,k})$$

$$\widehat{\boldsymbol{a}}_{j,k}^{hard} = \frac{\alpha_{j,k}}{\sqrt{2}} (sign(\chi) + i sign(\psi))$$

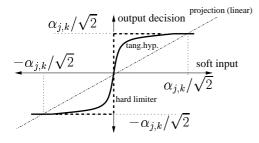


Figure 6: Linear, hyperbolic tangent and hard limiter decisions for real/imaginary parts of QPSK symbols.

#### 4. SIMULATIONS

The  $K_i$  users of base station j use the same spreading factor and pass through the same downlink channel  $h_i$  which is a FIR filter, being the convolution of a sparse Vehicular A UMTS channel and a root-raised cosine pulse shape with roll-off factor of 0.22. The channel length is N = 19 chips at the UMTS chip rate of 3.84 Mchips/sec. An oversampling factor of m = 2 and one receive antenna q = 1 are used. User symbols are from a QPSK constellation. The user of interest has 10dB less power than the average interferer power, which represents a near-far situation. Fig. 7 and Fig. 8 show the SINR performance of the proposed receivers and the classical reference Rake receiver. As expected, they outperform the Rake receiver with high margins. Hard and hyperbolic tangent non-linearities give better results than the projector, hyperbolic tangent performing slightly better [5]. As for the linear filter after the subtractor, the MMSE filter which is constructed based on the residual interference powers from the two IC branches outperforms the Rake receiver.

## 5. REFERENCES

 E. Kanterakis S. Moshavi and D. L. Schilling, "Multistage linear receivers for ds-cdma systems," *International Journal of Wireless Information Networks, Vol.3, No.1*, 1996.

- [2] C. Papadias and D.T.M. Slock, "Fractionally spaced equalization of linear polyphase channels and related blind techniques based on multichannel linear prediction," *IEEE Transactions on Signal Processing, Vol.47, No.3*, March 1999.
- [3] M. Lenardi and D.T.M. Slock, "A rake receiver with intracell interference cancellation for a ds-cdma synchronous downlink with orthogonal codes," *Proc. VTC* 2000, Tokyo, Japan, May 2000.
- [4] M. Lenardi and D.T.M. Slock, "Downlink intercell interference cancellation in wcdma by exploiting excess codes," *Proc. SAM 2000, Boston, Massachusset - USA*, March 2000.
- [5] A. Nahler R. Irmer and G. Fettweis, "On the impact of soft decision functions on the performance of multistage parallel interference cancelers for cdma systems," *Proc. VTC Spring 2001, Rhodes, Greece*, May 2001.

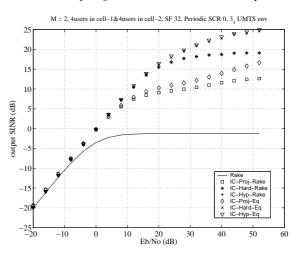


Figure 7: Output SINR vs Eb/No, 12.5% loaded BSs, nearfar situation, aperiodic scrambling, MMSE filters.

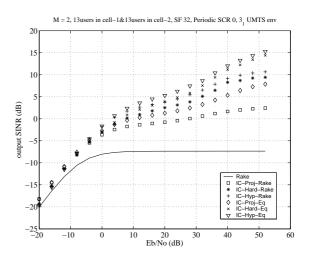


Figure 8: Output SINR vs Eb/No, 40% loaded BSs, near-far situation, aperiodic scrambling, MMSE filters.